


<b>Search Notes</b>  	<b>Application/Control No.</b>  10681206	<b>Applicant(s)/Patent Under Reexamination</b>  FUJI ET AL.
	<b>Examiner</b>  Jacob, Mary C	<b>Art Unit</b>  2123

<b>SEARCHED</b>			
<b>Class</b>	<b>Subclass</b>	<b>Date</b>	<b>Examiner</b>
703	14	9/4/07	MCJ

<b>SEARCH NOTES</b>		
<b>Search Notes</b>	<b>Date</b>	<b>Examiner</b>
Updated Inventor Name Search, Assignee Name Search: East, Google Scholar, IEEE	9/4/07	MCJ
Updated East Search: See search history	9/4/07	MCJ
Google Scholar: FPGA simulation "direction control signal" acceleration OR accelerator; FPGA simulation acceleration "direction control"; FPGA simulation acceleration connector interface OR bridge "bi-directional"; hitachi fpga accelerator signal direction; renesas fpga accelerator signal direction	9/4/07	MCJ
IEEE: ((simulat?? <near/2> accelerat???) <and> signal <near/2> direction; simulation acceleration and drivability;	9/4/07	MCJ

<b>INTERFERENCE SEARCH</b>			
<b>Class</b>	<b>Subclass</b>	<b>Date</b>	<b>Examiner</b>